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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/932,099	08/20/2001	Takayuki Kawahara	520.40524X00	5497

20457 7590 07/09/2004

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EXAMINER

CHEN, TSE W

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 07/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/932,099	KAWAHARA ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Tse Chen	2116	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 20 August 2001.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

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**Attachment(s)**

- |   |   |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                        | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Specification*

1. The disclosure is objected to because of the following informalities: IP is erroneously specified as Intellectual Properties on line 12 of page 1. Does applicant meant to have IP stand for Integrated Processors instead? Appropriate correction is required.
2. Claims 12-14 are objected to because of the following informalities: "CPU" should be detailed as to what it represents in order to avoid confusion with another like acronym. Appropriate correction is required.
3. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

### *Claim Rejections - 35 USC § 112*

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:  

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
5. Claims 1-14 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
6. The claims are generally narrative and indefinite, failing to conform with current U.S. practice. They appear to be a literal translation into English from a foreign document and are replete with grammatical or idiomatic errors. Examples are illustrated in the following.
7. As per claim 1, in reference to "performing predetermined processing based on the number of clocks" on lines 3-4, does the number of clocks refer to clock frequencies, clock

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sources or clock counts (ticks)? The term "clocks" used throughout the claims should be restated in more acceptable common art terminology.

8. As per claim 3, in reference to "no said input to said plurality of functional circuit blocks" on lines 2-3, is the input being detected in relation to all the circuit blocks or *one* of the circuit blocks? To avoid antecedent problem in relation to claim 1, the input should be detected based on *one* of the circuit blocks because the power status control circuit on line 3-4 that utilizes the input status should correspond with claim 1 "for controlling a power status of *each* of said plurality of functional circuit blocks."

9. As per claim 4, the paragraph "said prediction circuit ... said counting" on lines 8-15 is incomprehensible. There appears to be multiple elements involving a counter, a control means and a detection circuit. The elements should be clearly defined in their relationships to each other so the claim can be interpreted correctly in its metes and bounds.

10. As per claim 6, the paragraph "said prediction circuit ... number of clocks" on lines 10-17 is incomprehensible. There appears to be multiple elements involving a switch control circuit, a control signal line and an operating voltage setting circuit. Is the input being directed to the plurality of functional circuit blocks or just one of them? Is the input being directed to *both* the circuit blocks and clocks (sources)? The elements should be clearly defined in their relationships to each other so the claim can be interpreted correctly in its metes and bounds.

11. As per claim 8, does "it" on line 15 represent the number of clocks or the power?

12. Art is not being provided as there is confusion and uncertainty as to the proper interpretation of the claim language. See MPEP 2173.06.

**Conclusion**

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tse Chen whose telephone number is (703) 305-8580. The examiner can normally be reached on Monday - Friday 9AM - 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (703) 308-1159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tse Chen  
June 30, 2004



**REHANA PERVEEN**  
**PRIMARY EXAMINER**

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